

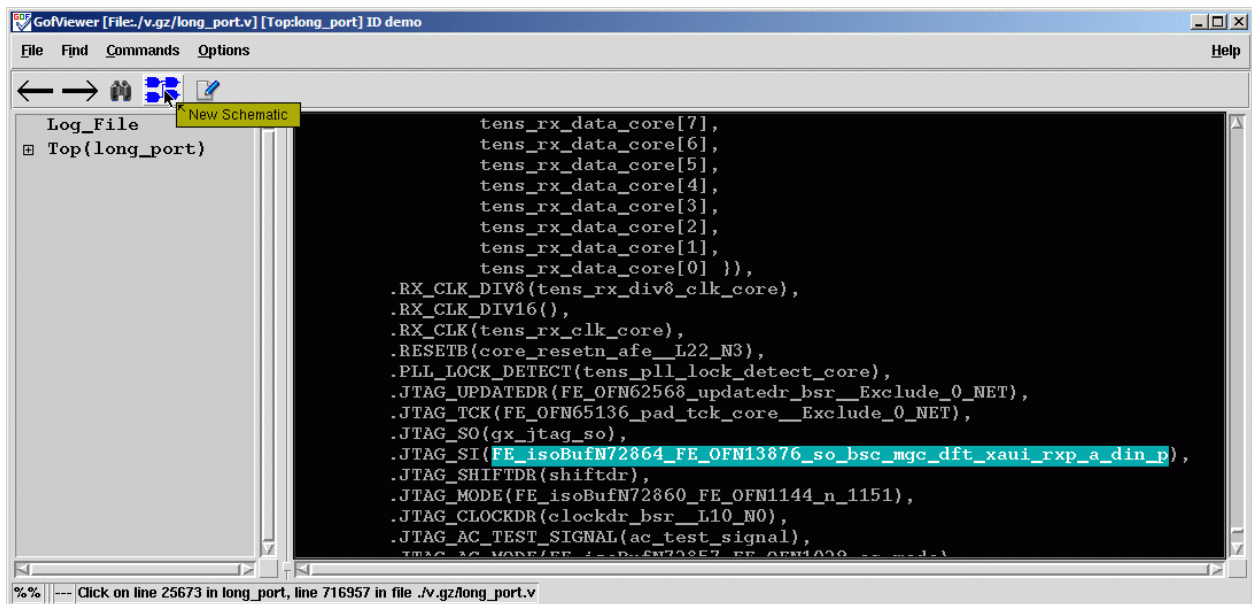
Debug connectivity by using Gates On the Fly

Contents

Getting started.....	1
Locate the point.....	2
Trace the connectivity.....	3
More debug.....	4

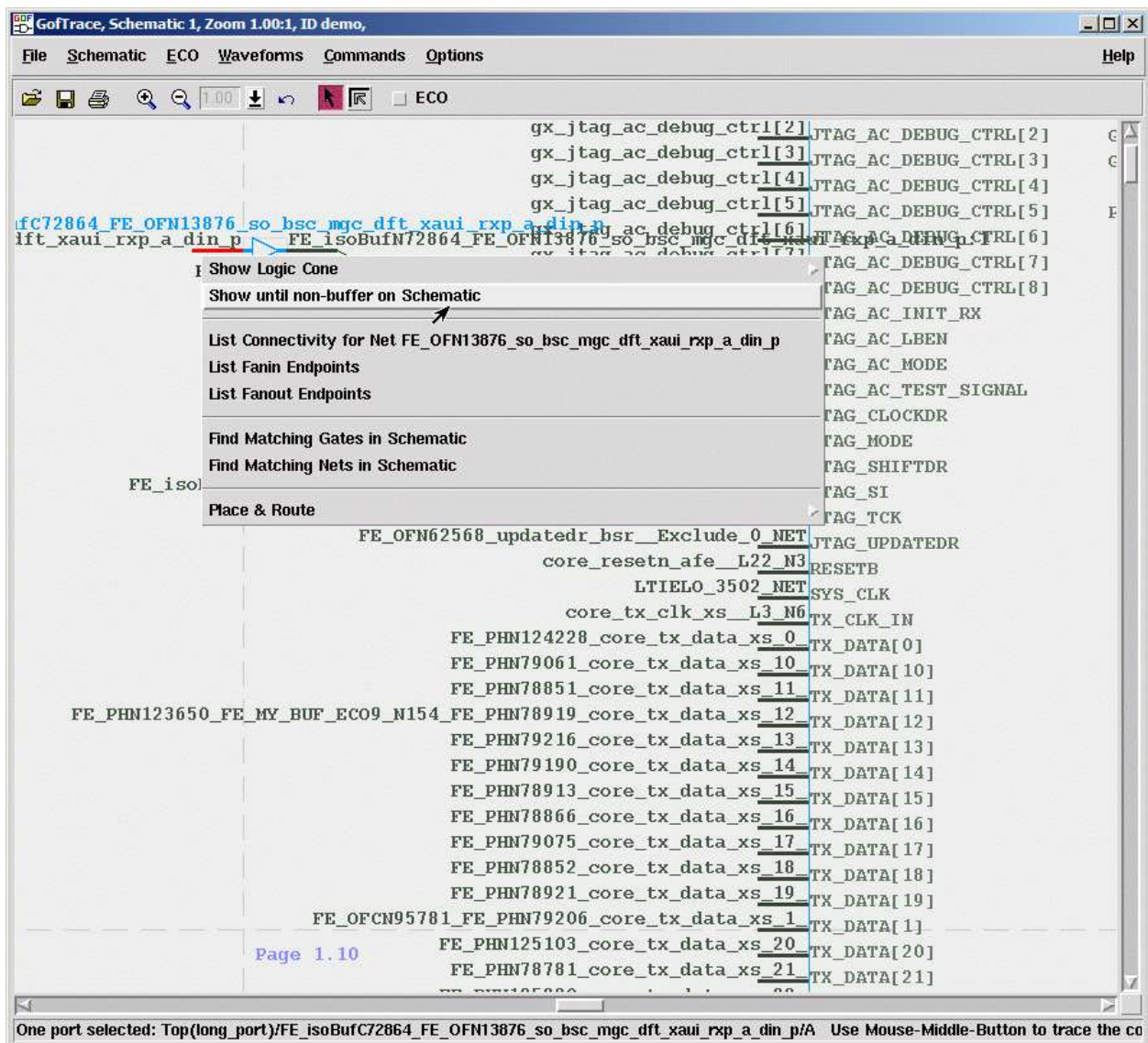
Getting started

- Use command line to load library and netlist
- `gof -lib tsmc.lib netlist.v`
- Use ctrl-f to find the net in GofViewer window
- Left-click the net to mark the whole net
- Click 'New Schematic' button



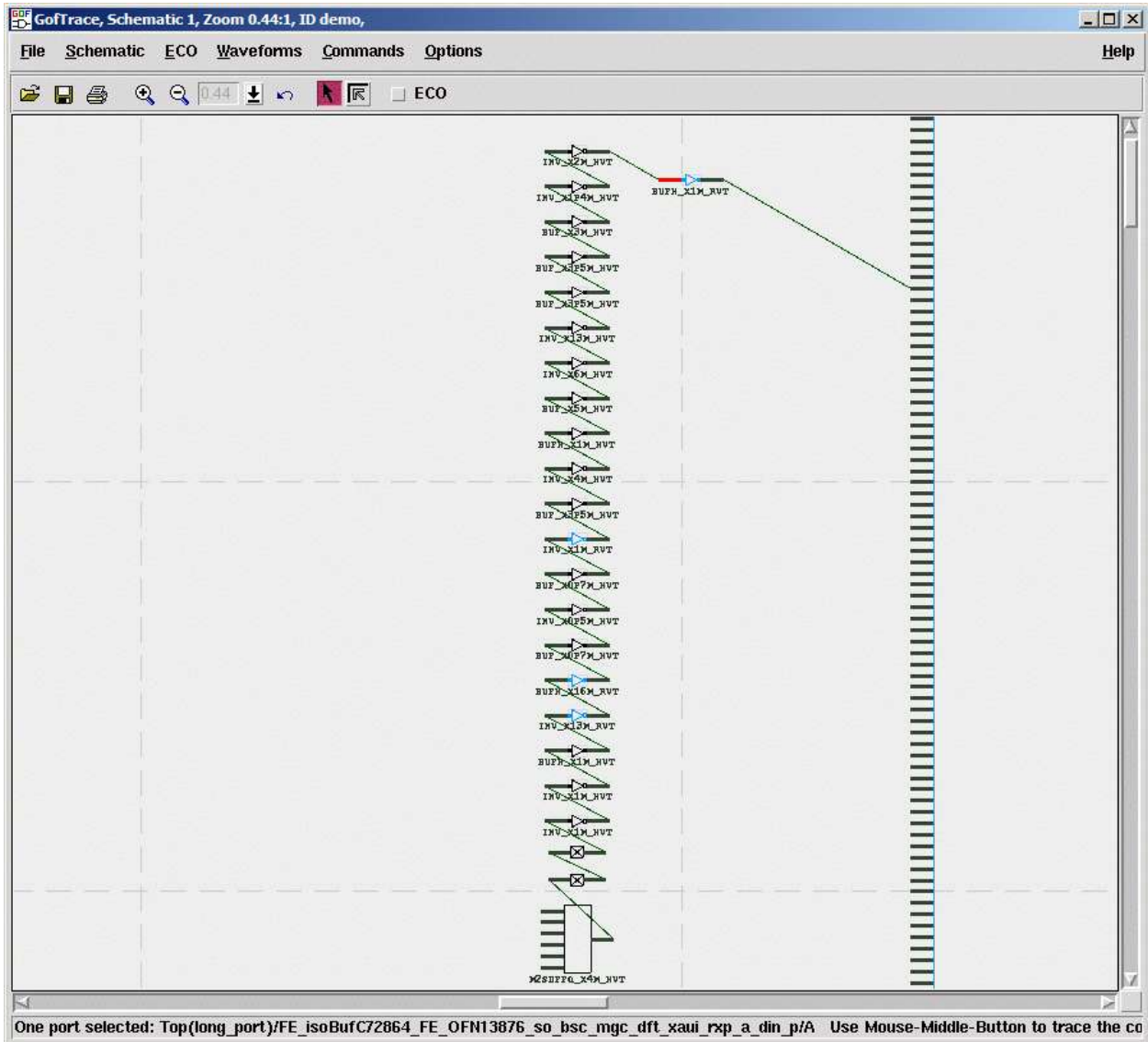
Locate the point

- Mouse middle-button click on input/output pins of a cell to expand the schematic
- In case of P&R netlist, one path may have lots of buffers/inverters
- Use 'Show until non-buffer on Schematic' to speed up the trace
- Mouse right-button click on the schematic to pop up menu



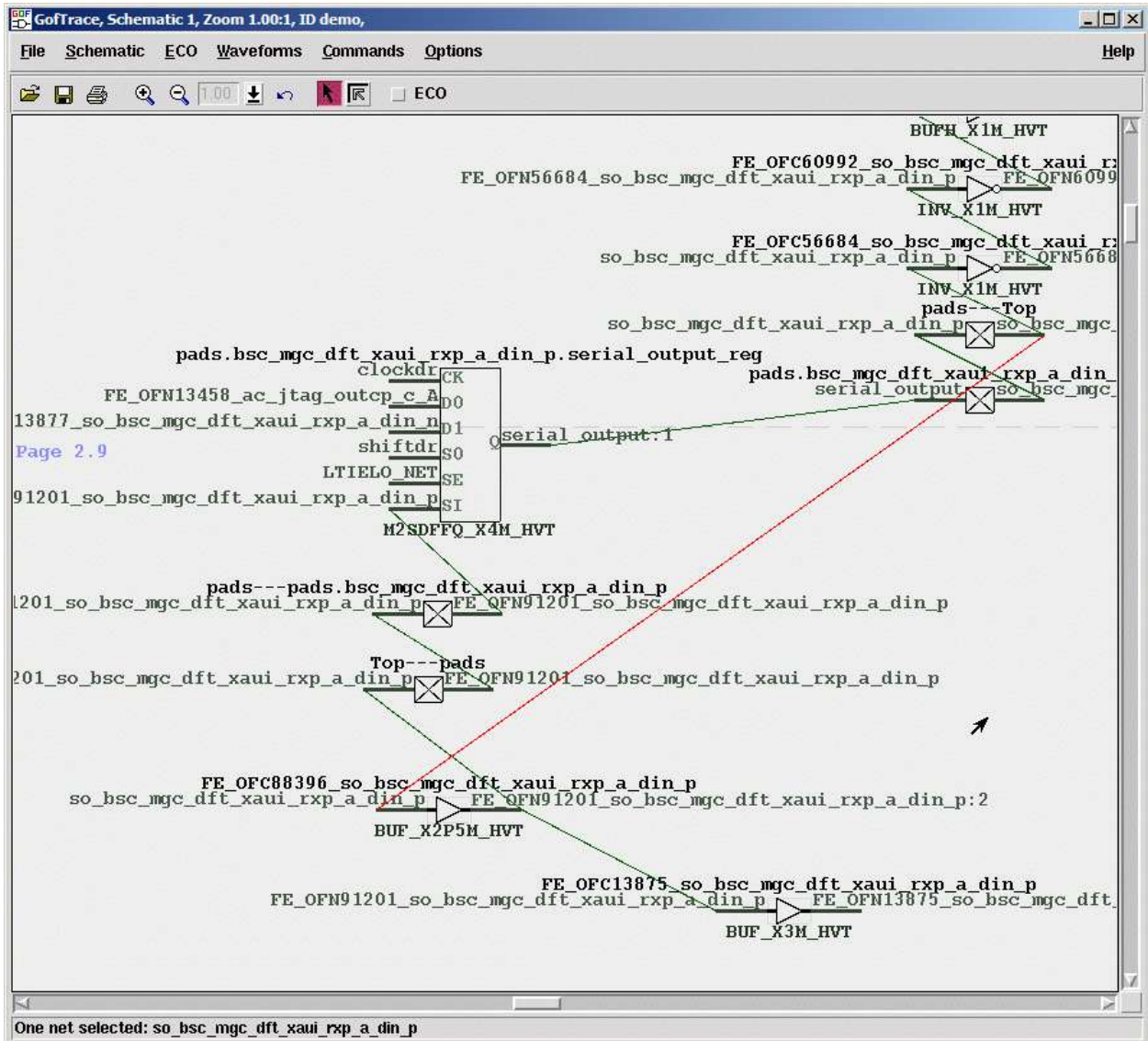
Trace the connectivity

- The circuit is traced until non-buffer or inverter
- Zoom out to view the whole path



More debug

- Do more trace by mouse middle button click on input output pins
- You can use delete key to remove not used gates and wires



Gates On the Fly Use Case: Debug connectivity

- The schematic can be saved in PS or PDF file format for record
- You can copy the interested part to a new schematic to start a new debug

